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				Application Number	10/787,443
				Filing Date	February 26, 2004
				First Named Inventor	Steven Koenck
				Group Art Unit	16
				Examiner Name	Dady Chery
				Attorney Docket Number	14425US02
Sheet	1	of	1		

U.S. PATENT DOCUMENTS					
Examiner Initial*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	A58	5,241,542	08/31/1993	Natarajan et al.	
	A59	4,745,408	05/17/1988	Nagata et al.	
	A60	5,142,279	08/25/1992	Jasinski et al.	
	A61	5,257,019	10/26/1993	Schwendeman et al.	
	A62	5,150,361	09/22/1992	Wieczorek et al.	
	A63	4,799,253	01/17/1989	Stern et al.	
	A64	5,252,963	10/12/1993	Snowden et al.	
	A65	5,361,397	11/01/1994	Wright	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

OTHER ART -- NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	C317	LO et al., "An Estimate Of Network Database Transaction Volume To Support Personal Communications Services," IEEE - ICUPC '92 Proceedings, pp. 236-241, 09/29/1992-10/02/1992
	C582	LO, "A 1.5-V 900-MHz Monolithic CMOS Fast-Switching Frequency Synthesizer For Wireless Applications," Symposium on VLSI Circuits of Technical Papers, p. 238-241, 2000
	C583	ALVAREZ, A Wide-Bandwidth Low-Voltage PLL for PowerPC Microprocessors, Symposium on VLSI Circuits Digest of Technical Papers, 1994

EXAMINER SIGNATURE		DATE CONSIDERED	
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